做科研，非一朝一夕
买器材，应速战速决

Newport数千种优质产品当日发货，
更多惊喜尽在PhotonSpeed™光速购！
Recent progress in attenuation counterpropagating optical phase-locked loops for high-dynamic-range radio frequency photonic links [Invited]

Shilei Jin, Longtao Xu, Peter Herczfeld, Ashish Bhardwaj, and Yifei Li

1Department of Electrical and Computer Engineering, University of Massachusetts Dartmouth, 265 Old Westport Rd., Dartmouth, Massachusetts 02032, USA
2Department of Electrical and Computer Engineering, Drexel University, 31st and Market Street, Philadelphia, Pennsylvania 19104, USA
3Department of Electrical and Computer Engineering, University of California Santa Barbara, Santa Barbara, California 93106, USA
4Transmission Components Research, JDSU Corporation, 430 N. McCarthy Blvd, Milpitas, California 95035, USA
*Corresponding author: yili2@umassd.edu

Received April 2, 2014; revised May 31, 2014; accepted June 1, 2014; posted June 4, 2014 (Doc. ID 208369); published July 14, 2014

In order to achieve small size, light weight, and immunity to electromagnetic interference, it is desirable to replace bulky coaxial cables with optical fiber in advanced radar front-ends. Such applications require a large dynamic range that is beyond the reach of conventional intensity modulation—direct detection fiber-optic links. A coherent fiber-optic link employing an optical phase-locked loop (OPLL) phase demodulator has been proposed as a solution to this problem. The challenge is the practical realization of the OPLL demodulator that satisfies the stringent loop delay requirement. A novel attenuation counterpropagating (ACP) OPLL concept has been proposed and demonstrated as a solution. In this paper we review the recent progress in realizing chip-scale ACP-OPLL devices. In particular, we focus on the latest measurement results achieved using a hybrid integrated ACP-OPLL, as well as the design and performance potential of a monolithically integrated ACP-OPLL photonic integrated circuit. © 2014 Chinese Laser Press

OCIS codes: (060.2360) Fiber optics links and subsystems; (060.5060) Phase modulation; (250.5300) Optical phase-locked loops (OPLL) phase demodulator (see Fig. 1)

1. INTRODUCTION

Restricted front-end spurious free dynamic range (SFDR) is a common limiting factor in many radar receivers [1–7]. Conventional electronic receiver front-ends rely on transistors and diodes for signal amplification and frequency mixing. Both transistors and diodes contain inherent nonlinearities, resulting in limited SFDR. On the other hand, photonic radar front-ends employ RF/photonics links. Prevalent RF/photonics links employ intensity modulation (IM) schemes that are nonlinear and thus also restrict the dynamic range. Various linearization methods, such as predistortion and postdistortion linearizers, have been explored for improving the SFDR of electronic amplifiers and mixers [1–3]. However, the SFDR enhancement is small, and the bandwidth is narrow in these cases. Linearizers have also been applied to RF/photonics links with similar results [4–7].

To solve the problem of limited SFDR, a linear phase-modulated RF/photonics link with an optical phase-locked loop (OPLL) phase demodulator (see Fig. 1) has been proposed [8,9]. It relies on the notion that optical phase modulation (PM) can be highly linear. Thus, using a linear phase demodulator, a high-dynamic-range RF/photonics link can be realized. The most critical part of the link is the OPLL linear phase demodulator. The OPLL demodulates the optical phase by tight phase tracking. It must have a large open loop gain over a wide bandwidth. For feedback stability, the OPLL only tolerates very short loop propagation delay [8]. This delay requirement can be met by employing an OPLL concept with attenuating counterpropagating (ACP) optical and RF fields [10]. Chip-scale ACP-OPLL devices have been implemented using both hybrid [8] as well as monolithic integration [11]. It is to be noted that hybrid integration refers to a device, where different components of a device can be designed on different chips, whereas monolithic integration refers to a device where all of its components are monolithically integrated onto a single chip. Hybrid integration is advantageous as it enables the use of suitable materials for realizing the internal components of the OPLL. On the other hand, monolithic integration allows for shorter loop propagation delay and consequently a larger ACP-OPLL bandwidth than what can be achieved using hybrid integration. Monolithic integration has the potential to improve the ACP-OPLL’s power and cost efficiencies. In this paper we review the recent progress in developing integrated ACP-OPLLS. We put emphasis on the new experimental results obtained from a hybrid integrated ACP-OPLL as well as the design and performance that can be achieved using a monolithically integrated ACP-OPLL. Both of these topics have not been discussed in previous publications.
2. ATTENUATION COUNTERPROPAGATING OPTICAL PHASE-LOCKED LOOP CONCEPT

As shown in Fig. 1, an ACP-OPLL contains an ACP phase modulator pair, a compact 3 dB coupler, and a high-power-balanced photodetector (BPD) pair. The key here is the ACP phase modulator (see Fig. 2). In the ACP phase modulator, the optical field and the modulation RF field counterpropagate, and the RF field experiences strong attenuation. The modulator propagation delay is thereby eliminated, and consequently the OPLL feedback stability is improved. Through the feedback, the ACP-OPLL demodulates the incoming differential optical phase by tightly tracking the optical phase. The theoretical performance of a PM optical link employing the ACP-OPLL was studied using a system model [8] shown in Fig. 3. In Fig. 3, \( P_{\text{opt}} \) is the optical power of each optical path entering the OPLL, \( \theta_{\text{in}} \) represents the incoming differential phase that carries information, \( R_{\text{pd}}(\omega) \) is the responsivity of the photodiode, \( Z_{\text{pd}} \) is the termination impedance of the photodiode, \( \beta_{\text{TX}}(\omega) \) and \( \beta_{\text{ACP}}(\omega) \) are the PM sensitivities of the transmitter (TX) and the ACP phase modulator pair, respectively, and \( \tau_d \) is the propagation delay due to the 3 dB coupler and the feedback path. The phase delays for the photodetector and the local phase modulator are included in \( R_{\text{pd}}(\omega) \) and \( H_{\text{ACP}}(\omega) \), respectively. \( \delta I_{\text{shot}} \) is the shot noise of the BPD:

\[
\delta I_{\text{shot}} = 2 \sqrt{I_{\text{pd}} \cdot e},
\]

where \( e \) is the charge of a single electron and \( I_{\text{pd}} \) is the DC photocurrent of each photodetector; that is, \( I_{\text{pd}} = R_{\text{pd}} \cdot P_{\text{opt}} \).

Using Fig. 3, we first determine the link gain in the small signal condition (\( \sin(\theta_{\text{in}}) \sim \theta_{\text{in}} \)) to be

\[
G_{\text{link}} = (\beta_{\text{TX}}/\beta_{\text{ACP}}) \cdot G_\alpha/(1 + G_\alpha),
\]

where \( G_\alpha \) is the ACP-OPLL open loop gain, given by

\[
G_\alpha(j\omega) = 2 \cdot \beta_{\text{ACP}} \cdot R_{\text{pd}} \cdot Z_{\text{pd}} \cdot P_{\text{opt}} \cdot e^{j\pi c}. 
\]

Also from Fig. 3, we determine the link noise figure (NF) in the photodetector shot noise limit to be

\[
NF = 10 \log_{10} [1 + e \cdot \beta_{\text{TX}}^2/(I_{\text{pd}} \cdot Z_{\text{TX}} \cdot K T)].
\]

where \( Z_{\text{TX}} \) is the termination impedance of the transmitter phase modulator electrode, \( K \) is the Boltzmann constant, and \( T \) is the environmental temperature.

The link SFDR in the photodetector shot noise limit is given by

\[
\text{SFDR} = \frac{40}{3} \log_{10} \left| \phi_{\text{IP3}}/\sqrt{e/I_{\text{pd}}} \right|.
\]

where \( \phi_{\text{IP3}} \) is the demodulation phase third-order intercept point (IP3) of the ACP-OPLL, given by

\[
\phi_{\text{IP3}} = \min\{\phi_{\text{IP3,DM}}, \phi_{\text{IP3,PM}}\},
\]

where \( \phi_{\text{IP3,DM}} \) is the demodulation phase IP3 set by the OPLL internal homodyne phase demodulation mechanism, and \( \phi_{\text{IP3,PM}} \) is the phase IP3 of the ACP phase modulator set by the modulator nonlinearity. They are given by

\[
\phi_{\text{IP3,PM}} = \sqrt{4 \cdot (G(\omega_0))^2 \cdot [1 + G(\omega_0)]},
\]

\[
\phi_{\text{IP3,PM}} = \phi_{\text{IP3,PM}} \cdot L.
\]

where \( L \) is the length of the ACP phase modulator, and \( \phi_{\text{IP3,PM}} \) is modulator phase IP3 per unit length. \( \phi_{\text{IP3}} \) is determined through measurements.

As shown in Fig. 4, in order to achieve a SFDR over 140 dB·Hz\(^{2/3}\), \( \phi_{\text{IP3}} \) should be larger than 24\( \pi \) assuming 50 mA of photocurrent per detector. Over 50 mA of photocurrent is a reasonable expectation for high-power unitraveling carrier (UTC) photodetectors prior to saturation [12, 13]. This \( \phi_{\text{IP3}} \) requirement necessitates an open loop gain of 20 dB or larger. To ensure feedback stability in presence of a large open loop gain over a wide bandwidth, the ACP-OPLL can only tolerate very small loop propagation delay (see Fig. 5).

Fig. 4. SFDR as a function of \( \phi_{\text{IP3}} \) and the photocurrent per photodetector.
Minimizing the loop delay is one of the most critical tasks for realizing the ACP-OPLL.

3. HYBRID INTEGRATED ATTENUATION COUNTERPROPAGATING OPTICAL PHASE-LOCKED LOOPS

Hybrid integration enables the loop components of the OPLL to be realized using the most suitable material systems and obtain superior optical performance for each optical functionality. In the current version of the hybrid integrated ACP-OPLL, as shown in Fig. 6, the ACP local phase modulator and the 3 dB coupler of the hybrid integrated ACP-OPLL are realized on a LiNbO$_3$ chip. LiNbO$_3$ is a suitable material to realize linear, low-loss optical phase modulators. The LiNbO$_3$ ACP phase modulator is designed to be 6.8 cm long. To achieve the desired attenuation of the RF modulation signal, the modulator employs a thin film electrode with a 100 Å Ti layer and a 1000 Å Au layer. The phase modulator has a measured $V_π$ of $\sim$1.9 V. The ACP-OPLL employs a conventional coupled-line 3 dB coupler. To minimize the delay, the output of the coupler is directly butt-coupled to a pair of UTC BPDs. Different from the previously reported hybrid ACP-OPLL [8], the ACP-OPLL in this paper contains a pair of back-illuminated UTC photodetectors. They sit on an AlN submount (see Fig. 7). This new BPD pair has improved responsivity and saturation optical power.

The ACP-OPLL was characterized using the setup shown in Fig. 8. The laser source is a 1550 nm single-frequency narrow-linewidth fiber laser (Orbits ETH-20-1550). Its output is amplified using an erbium-doped fiber amplifier (EDFA). In order to reduce the noise due to amplified spontaneous emission, the output of the EDFA is filtered by a narrow-band (BW = 1 nm) fiber Bragg grating (FBG). Then the light is split into two paths using a 3 dB optical coupler. One path contains a Tx optical phase modulator, which accepts the RF input. The other path contains the ACP optical phase modulator of the ACP-OPLL. The two paths are recombined using another 3 dB coupler and then are butt-coupled to the BPD pair. The output of the ACP-OPLL is extracted by a ground-signal-ground (GSG) probe through a 70 $\Omega$ resistor. Standard two-tone intermodulation tests were performed to evaluate the phase demodulation linearity (or OIP3) of the ACP-OPLL.

As shown in Fig. 9, initially the measured ACP-OPLL OIP3 was improved when increasing the photodetector current (or the input optical power). This is expected as larger photocurrent results in larger open loop gain, and consequently better OPLL phase demodulation linearity (see Eq. (7)). However,
OPTICAL PHASE-LOCKED LOOPS

Phase-Locked Loops Design

To mitigate these problems, we have developed a detuned shallow MQW phase modulator, a monolithically integrated ACP-OPLL should achieve higher operation frequency (~GHz) than that of a hybrid interested ACP-OPLL (~hundreds of MHz).

4. MONOLITHIC INTEGRATED ATTENUATION COUNTERPROPAGATING OPTICAL PHASE-LOCKED LOOPS

In order to shorten the loop delay and improve the power and cost efficiencies, monolithic integration of ACP-OPLL on an InP-based material platform was investigated [11].

A. Attenuation Counterpropagating Optical Phase-Locked Loops Design

A schematic of the monolithically integrated ACP-OPLL is shown in Fig. 12. It contains a pair of integrated InGaAsP multiquantum-well (MQW) phase modulators, a compact multimode interference (MMI) coupler, and a UTC waveguide photodetector. All OPLL components share a common quantum well optical waveguiding layer. Due to tighter integration and more efficient MQW phase modulator, a monolithically integrated ACP-OPLL should achieve higher operation frequency (~GHz) than that of a hybrid interested ACP-OPLL (~hundreds of MHz).

1. Push–Pull Balanced Attenuation Counterpropagating Phase Modulator

The main disadvantage of the InP material platform is the MQW phase modulator. Compared with the conventional LiNbO₃ phase modulators, the MQW phase modulator has a large optical propagation loss, parasitic IM, and nonlinearity. To mitigate these problems, we have developed a detuned shallow MQW modulator [14], which consists of 25 periods of lattice matched 9 nm thick In₀.₅₆Ga₀.₄₄As₀.₇₀P₀.₃₀ quantum wells and 6.5 nm thick In₀.₅₆Ga₀.₄₄As₀.₄₁P₀.₅₉ layers as shown in Fig. 12. To reduce the optical propagation loss and parasitic IM, the quantum well photoluminescence peak is set ~170 nm.

![Fig. 10. Output IMD: (a) output of an IM-direct detection (IM-DD) link with a MZ intensity modulator and (b) measured output spectrum of a hybrid ACP-OPLL in an identical modulation index condition.](image1)

![Fig. 11. SFDR measurements: (a) 50 MHz; (b) 100 MHz; (c) 200 MHz; (d) 300 MHz.](image2)

![Fig. 12. Schematic of a monolithically integrated ACP-OPLL.](image3)
away from the 1.55 μm operating wavelength. The low quantum well barrier height reduces the quantum confinement at high bias voltages. This helps to create a linear PM region near a certain bias (∼6 V). Using a 1 mm long shallow MQW phase modulator, phase IP3 of ∼4π, optical propagation loss of ∼0.9 dB (<1 dB/mm), and $V_x$ of ∼6 V were demonstrated. The linearity of a 3 mm long device in a balanced pair should have a combined phase IP3 of 24π. This is sufficient to support an ACP-OPLL with SFDR larger than 140 dB · Hz$^{1/2}$ as shown in Fig. 4.

However, the shallow MQW phase modulator still generates significant photocurrent for high optical input power, which presents a problem when implementing the ACP phase modulator as part of the OPLL. This is because the ACP modulator uses a lossy electrode with a large sheet resistance. If the photocurrent flows through the electrode, a significant voltage drop may be generated, resulting in a large bias voltage deviation from the optimum bias voltage (∼6 V). To solve this problem, we conceived a push–pull balanced ACP modulator configuration as shown in Fig. 14.

In Fig. 14, the left modulator has a lossy N-metal contact (electrode), and the right modulator has a lossy P-metal contact (electrode). The layer structures of the lossy N and P contacts are given by Ni–AuGe–Ni–Au (500–800–200–1200 Å) and Ti–Pt–Au (200–400–800 Å), respectively. Both metal contacts have a sheet resistance of ∼0.62 Ω². The two electrodes are connected by an array of metal stubs (10 μm wide). The separation between the adjacent stubs is 200 μm. The two phase modulators in Fig. 14 should generate complementing photocurrents. These photocurrents flow to the DC power supply through the metal stubs. No net photocurrent flow occurs on the lossy electrodes. Therefore, the optimum bias voltage is maintained along the modulator electrodes.

On the other hand, the connection stubs add discontinuity to the ACP-modulator electrode and consequently introduce reflection to the modulation signal on the electrode. Their impact to the performance of the push–pull ACP modulator was carefully studied. First, the reflection caused by a single stub was simulated using Ansoft HFSS, and the result is shown in Fig. 15. Below 20 GHz, the reflection is small, but it increases at higher frequency. Then, we simulated the frequency response of a 3 mm long ACP push–pull modulator pair with and without the reflection from these stubs. The 3 mm long ACP push–pull modulator pair contains 15 connection stubs. As shown in Fig. 16, the ACP modulator pair has a $V_x$ of ∼1.26 V and a 3 dB bandwidth of 1.15 GHz. In the phase response [Fig. 16(b)] the impact of the connection stubs is observed only when the frequency is larger than 200 GHz. This is 170 times larger than the modulator bandwidth. Thus, we conclude that the connection stubs should not cause any penalty in the phase margin of the ACP-OPLL.

2. Balanced Counterpropagating Waveguide Unitraveling Carrier Photodetector

The layers that define the UTC-waveguide photodetector for the monolithic integrated ACP-OPLL are developed by
Klaknkin et al. [12], and are grown on top of the MWQ optical waveguide as shown in Fig. 12. The design of the BPD pair is shown in Fig. 17. To reduce its latency, the photodetector employs counterpropagating optical and RF fields [15]. The width and the length of the photodetector are 10 and 200 μm, respectively. The overlap between the absorbing layers of the detector and the optical mode is set to 1.4%. The detector absorption length (with 95% absorption) is ∼168 μm (see Fig. 18).

Figure 19 shows the frequency response of the photodetector output voltage when it is connected to the ACP phase modulator pair. The response depends on the photodetector’s load impedance. Its 3 dB bandwidth is ∼1.3 GHz with conventional 50 Ω load impedance. If the load impedance is raised to 300 Ω, the bandwidth is reduced to ∼0.4 GHz. However, the higher load impedance generates larger amplitude response. This helps to increase the OPLL open loop gain.

3. Compact 2 × 2 Multimode Interference Coupler

The monolithic integrated ACP-OPLL uses a regular MMI coupler (see Fig. 20). The length of the coupler is 217 μm. The MMI coupler sets the total OPLL loop delay to be ∼10 ps, which includes propagation delays of the MMI coupler, waveguide bends, tapers, and the feedback electrode.

B. Performance Projection

The layout of a complete monolithic integrated ACP-OPLL photonic integrated circuit (PIC) is depicted in Fig. 21. The projected SFDR and NF performance in the photodetector shot noise limit were simulated, and the results are
summarized in Figs. 22 and 23, respectively. In this simulation the BPD of the ACP-OPLL has conventional 50 Ω output load impedance, and the link transmitter phase modulator has a $V_{π}$ of 1 V. The projected SFDR scales with the photocurrent. When the photocurrent reaches 60 mA per photodetector, the ACP-OPLL PIC should achieve $\sim 140 \text{ dB} \cdot \text{Hz}^{2/3}$ SFDR over 1 GHz bandwidth. In this scenario, the link NF should reach 4 dB. Figure 24 depicts stable phase margin of the ACP-OPLL as a function of the photocurrent. Even with 80 mA photocurrent, which represents a 22.9 dB open loop gain, the ACP-OPLL still has a phase margin greater than 40°.

C. Measurement of the Monolithically Integrated Attenuation Counterpropagating Optical Phase-Locked Loops

The fabricated ACP-OPLL PIC is shown in Fig. 25. Details on the device fabrication and measurement were discussed in [16] and [11], respectively. It was found that the UTC waveguide photodetectors on the fully fabricated ACP-OPLL PICs demonstrated smaller responsivity and narrower bandwidth compared to what could be expected from these devices (see Fig. 19). Only 15 mA photocurrent was generated in each photodetector of the ACP-OPLL PIC, which limited the performance of the ACP-OPLL PIC. Figure 26 shows the output of the ACP-OPLL PIC in a two-tone intermodulation test at 200 MHz. The distortion is 39 dB better than that of a conventional MZ modulator under a similar modulation index condition. The measured SFDR of the ACP-OPLL PIC is $\sim 130 \text{ dB} \cdot \text{Hz}^{2/3}$ over 300 MHz as reported in [11].
ACKNOWLEDGMENTS

The authors would like to thank Dr. Gary Evans for supplying the back illuminated UTC photodetectors. The authors would like to also thank Dr. Jonathan Klamkin, Dr. Leif Johansson, Dr. John Bowers, and Dr. Larry Coldren at UCSB for their support in developing the monolithically integrated ACP-OPLLs.

REFERENCES


