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Integrated dispersion compensated mode-locked quantum dot laser

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Received 8 May 2020; revised 17 June 2020; accepted 30 June 2020; posted 7 July 2020 (Doc. ID 397175); published 7 August 2020

Quantum dot lasers are excellent on-chip light sources, offering high defect tolerance, low threshold, low temperature variation, and high feedback insensitivity. Yet a monolithic integration technique combining epitaxial quantum dot lasers with passive waveguides has not been demonstrated and is needed for complex photonic integrated circuits. We present here, for the first time to our knowledge, a monolithic offset quantum dot integration platform that permits formation of a laser cavity utilizing both the robust quantum dot active region and the versatility of passive GaAs waveguide structures. This platform is substrate agnostic and therefore compatible with the quantum dot lasers directly grown on Si. As an illustration of the potential of this platform, we designed and fabricated a 20 GHz mode-locked laser with a dispersion-engineered on-chip waveguide mirror. Due to the dispersion compensation effect of the waveguide mirror, the pulse width of the mode-locked laser is reduced by a factor of 2.8.

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https://doi.org/10.1364/PRJ.397175

1. INTRODUCTION
Quantum dot (QD) lasers bring several distinct advantages over their quantum well (QW) counterparts. First proposed in 1982 as temperature-insensitive lasers [1], QD lasers hold the record for highest continuous-wave (CW) operating temperature (220°C) due to the dots having discrete energy levels [2]. The reduced density of states gives rise to reduced transparency carrier density, which translates to low threshold current [3]. The atom-like dot structure possesses a symmetric delta-like gain spectrum, which corresponds to near-zero linewidth enhancement factor [4]. This in turn manifests as optical feedback insensitivity and ultranarrow linewidth [5,6].

Perhaps the most notable characteristic of the QD lasers is their high tolerance to growth defects due to locally confined carrier in each dot [7]. Such high tolerance enables epitaxial growth of QD material on CMOS-compatible substrates, such as on-axis (001) silicon (Si). Epitaxial growth on large Si substrates brings the benefits of significantly reduced cost and potential high-density integration of QD lasers in photonic integrated circuits (PICs) [8]. Significant progress has been made in this direction in the past few years for QD lasers grown on Si, with reliability and laser performance approaching those of QD lasers grown on native substrate [7,9,10]. A variety of devices have been demonstrated including Fabry–Perot (FP) lasers, distributed feedback (DFB) lasers [11], mode-locked lasers (MLLs) [12], microring lasers [13], coupled cavity tunable lasers [14], and photodetectors (PDs) [15].

Yet, all the aforementioned devices are stand-alone devices, and no integration of such devices with monolithic PICs has been reported. This issue stems from the lack of a passive waveguide (WG) in the current epitaxial QD on the Si device platform. Indeed, the versatility of passive WG structures is imperative in building successful laser integration platforms, as in the case of InP PICs [16], heterogeneous III-V on Si PICs [17], or recent heterogeneous III-V/Si/Si3N4 PICs [18]. In the QD laser on a Si platform, proposals for integration with Si WGs have been made [7,8]. One of them suggests coupling light from laser active region downward through several micrometers of III-V buffer layers to reach the Si layer; the other requires growing QD lasers in a trench and aligning the active WG with the passive WG in height. Both are challenging in terms of growth and optical coupling element design. Another approach involves heterogeneous integration of QDs on a silicon-on-insulator (SOI) wafer via direct bonding. This approach is readily achievable given that lasers from bonding of native substrate QD lasers on SOI wafers have already been demonstrated [19]. However, our goal is scaling the entire process to 300 mm Si wafers and achieve the ultimate cost efficiency [20].

Here we showcase a novel passive-active integration scheme for QD on Si, which we term the offset quantum dot (OQD)
device platform. The concept is similar to the offset quantum well (OQW) technique widely used in InP PICs [21]. Both approaches involve shifting the active regions upward from the center of the intrinsic region of the laser epitaxy. By doing so, the lower half of the intrinsic region can be utilized as a passive WG layer to form a laser cavity or other passive WG structures for light partitioning, routing, or switching purposes. The differences of the OQD scheme from its QW counterparts are twofold. First, instead of multiple regrowths, the OQD approach fully establishes all the contact and passive layers within a single epitaxy growth, significantly simplifying the fabrication procedure. Second, the QD active region’s thickness (>200 nm) is considerably larger than that of QW active region (~100 nm). This results in much larger optical mode mismatch between the active and passive WGs in OQD devices, necessitating careful design of the active-passive WG transition, not existing in offset QW devices. The OQD platform is able to scale with the size of the cheap Si substrate by fully exploiting the cost reduction of larger wafer size. Furthermore, the OQD platform benefits from a simple fabrication flow that does not require multiple regrowths as in the monolithic InP laser platform or wafer bonding as in the heterogeneous laser platform, further facilitating cost savings. To demonstrate the potential of the OQD device platform, we design and fabricate the first epitaxial passively mode-locked OQD lasers. These lasers have one mirror formed by sidewall gratings etched on the passive GaAs WG. These gratings are chirped and apodized to compensate for the group velocity dispersion generated in the active section of the lasers. These lasers have a repetition rate designed at 20 GHz. The minimum threshold is 58 mA with saturable absorber (SA) left floating. The minimum pulse width is 4.5 ps, representing a factor of 2.8 reduction compared to MLLs without dispersion compensation.

2. Epitaxy Design and Device Fabrication

The InAs OQD laser growth is performed on a native GaAs substrate by solid-source molecular beam epitaxy. Although the OQD platform is primarily designed for QD lasers on Si, as will be shown later, the fabrication procedure is entirely substrate agnostic and can readily be implemented on lasers grown on any substrate. Figure 1(a) shows the detailed QD epitaxial structure. The laser epitaxy is similar to what has been detailed in Ref. [10] with several distinctions in the intrinsic region. The growth starts with a 500 nm heavily n-type doped GaAs contact layer and a 1400 nm n-type AlGaAs cladding layer. 30 nm of Al0.2Ga0.8As follows the n-type cladding to function as the second etch stop during fabrication. It was then followed by 230 nm of intrinsic GaAs WG layer and another 20 nm of Al0.2Ga0.8As as the first etch stop during fabrication. Afterward, five layers of InAs/In0.15Ga0.85As dots-in-a-well (DWELLs) active regions with 37.5 nm GaAs spacers are grown. A 10 nm 5 × 10^{17} cm^{-3} p-modulation-doped GaAs layer was also introduced to each spacer to help improve the gain and temperature performance of the laser. The thick GaAs spacer layers are the main reason for the considerably larger passive-active WG mode mismatch in OQD devices compared to OQW devices. After the active region deposition, the whole growth procedure is completed with a 1400 nm p-type AlGaAs cladding and a 300 nm heavily doped p-type GaAs contact layer. The photoluminescence spectrum is centered at 1267 nm with a full width at half-maximum (FWHM) of 56 nm corresponding to homogeneous broadening 17 meV [22]. The dependence of optical confinement on the thickness of the passive GaAs WG is shown in Fig. 1(b). When the passive GaAs is 50 nm, the epitaxial structure is almost identical to the previously reported QD lasers [9,10], with the exception of the thin etch stop A. Thicker GaAs WG in OQD dilutes the optical confinement in the DWELLs. However, since the optical mode becomes more confined in the intrinsic region, the decrease in confinement in the DWELLs when the GaAs WG layer is 230 nm is only ~20%, despite the doubling of intrinsic region volume. When the GaAs WG layer is thinner than 230 nm, the mode overlap with the highly doped n-contact layer becomes large enough to make the optical loss significant in the passive WG. The larger minimum bending radius resulting from a thinner passive GaAs WG core also enlarges device footprints. Balancing these trade-offs, a design choice is made to set the passive GaAs WG layer thickness to be 230 nm.

OQD devices were fabricated on a quarter of a 3 inch (76 mm) GaAs wafer after molecular beam epitaxy (MBE) growth. First, the mesa pattern of the active section is defined by an oxide hard mask with i-line lithography, which is used throughout the fabrication process except as otherwise mentioned. The pattern is then transferred down to the III-V material with

![Fig. 1.](image-url)
a two-step inductively coupled plasma reactive ion etch (ICP-RIE). This two-step ICP-RIE process aims to remove all the active material above the first etch stop layer outside of the mesa region. The first ICP-RIE step uses a nonselective Cl₂/N₂ etch chemistry that stops ~100 nm above the first etch stop assisted by a laser etch monitor [Fig. 2(b)]. The second ICP-RIE step uses a highly selective BCl₃/SF₆/N₂ etch chemistry that precisely stops the etch on the first Al₀.₂Ga₀.₈As etch stop layer sandwiched between the active regions and the GaAs passive WG layer. This etch recipe [23] is optimized to have GaAs to Al₀.₂Ga₀.₈As etch selectivity of 20 to 1, with Al₀.₂Ga₀.₈As etch rate of 3 nm/min. It cannot be stressed enough that having this selective etch is crucial to the success of this OQD laser platform. Had only the nonselective etch been used for defining the ~2000 nm deep mesa, the etch nonuniformity would cause either residual QD layers on top of the GaAs WG or significantly over-etched WG layer. Both results are conducive to large WG propagation loss and variation of WG dimensions across the wafer. After the mesa etch, electron beam lithography and i-line lithography are used to define the grating and WG pattern, respectively, on the same oxide hard mask. Redundancy has been included in the grating pattern to provide tolerance to misalignment in the i-line lithography. The combined grating and WG pattern is then used to etch the WG structure, utilizing the same ICP-RIE steps detailed earlier, stopping on the second Al₀.₂Ga₀.₈As etch stop [Figs. 2(c) and 2(d)]. Afterward, the isolation between the SA and the gain section is created. Trenches around the mesa are etched to give access to the n-type contact. The last steps in fabrication are passivation and metallization to form contacts. The two etch stops guarantee precise and uniform WG dimensions across the wafer while eliminating the micro-trenching caused by the nonselective etch as seen in Fig. 2(b). Altogether, the OQD device platform is well poised for future development of flexible and variation-tolerant WG structures.

3. DEVICE DESIGN AND SIMULATION

A schematic representation of the MLL design is shown in Fig. 2(a). The laser primarily consists of four sections: an SA adjacent to a cleaved facet, a gain section with the passive-active transition taper, a short GaAs WG, and a grating mirror terminated by a WG spiral. The effective length of the laser is defined as the length of the first three sections of the laser plus the effective length of the grating. To achieve 20 GHz repetition rate, the effective length of the laser is set to be 2050 μm. The SA is set to be 20% of the effective cavity length. Special attention has been given to the active-passive taper transition in the laser design. The width of the mesa is set to be 3.5 μm, which is wide enough to avoid sidewall recombination [24]. The taper tip width is 0.5 μm, which can be reliably achieved by i-line lithography. The standard WG width is chosen to be 1.6 μm to maximize the optical confinement while maintaining a single-mode condition. In the taper gain section defined by the mesa etch, the mesa width is linearly varied over a length of 50 μm. During this transition, 10 μm from the taper tip, the WG layer linearly widens to 1.8 μm at the taper tip [Fig. 2(e)]. Afterward, the GaAs WG is tapered down to the standard width over 20 μm. The widened passive WG around the taper not only facilitates a downward optical power transition but also helps relax the alignment constraint between the two mask layers during fabrication. Finite-difference time-domain (FDTD) simulation has shown 1.4 dB passive transmission loss and -15 dB reflection for the aforementioned taper design.

For successful operation of the QD MLL, proper design of the grating mirror is important. Given the same time-bandwidth product (TBP), wider lasing bandwidth leads to shorter pulses. However, pulses from stand-alone semiconductor MLLs are always chirped due to self-phase modulation [25]. The dispersion in the gain section and the SA also reduces the locking bandwidth of an MLL, further broadening the pulse. Passive elements such as WG [26] or fiber [27] gratings have been integrated with QW MLLs to compensate for the dispersion generated by the active components. In QW MLLs, experimental investigations have found red-chirped pulses generated by active mode locking [26–29] and pulses in both chirping directions from passive mode locking [30–32]. For red-chirped pulses, negatively chirped gratings are required.

![Fig. 2.](image-url) (a) Schematic diagram of QD MLL above the bottom cladding. The rendering is not to scale. The WG spiral termination is not illustrated here for simplicity. Scanning electron microscope (SEM) images of (b) mesa cross section after the nonselective GaAs etch, (c) passive GaAs WG cross section with 1 μm thick silicon dioxide upper cladding, (d) GaAs WG with gratings etched on the sidewall, and (e) taper transition from the active to the passive WG section.)
to provide longer effective length for the blue end of the spectrum in order to compress the pulse. For blue-chirped pulses, the opposite is true. For QD MLLs, reports have shown red-chirped output from the gain section [33] with the contribution from the SA being negligible [34]. Based on this information, an apodized and negatively chirped WG grating has been designed. The total length of the grating is 200 μm. To chirp the grating, the average effective index of the WG is varied so that the Bragg condition of the grating changes at a chirp rate (CR) of 0.032 nm/μm. The negative sign indicates decreasing local Bragg wavelength in the direction away from the gain section. Apodization is implemented by modulating both the inner and outer profiles of the grating recesses with a Gaussian function, so that the average WG effective index profile still conforms to that required to achieve the desired chirp rate [35]. The reflectivity and group delay responses of the grating designs have been studied with a transmission matrix model implemented in MATLAB. The results are plotted in Fig. 3. As can be seen, the uniform grating offers no compensation in the group delay, while all the negatively chirped gratings generate more delay in the blue side of the spectrum around the Bragg wavelength. The necessity of apodization is apparent from the ripples in the pink curve in Fig. 3(a). These ripples degrade the flatness within the stopband of the grating, limiting the locking bandwidth of the MLL. These ripples are largely eliminated from the implementation of the Gaussian apodization profile in the gratings represented by the blue curve, at the price of less group delay dispersion. This can be compensated by making the gratings stronger as seen in the green curve in Fig. 3(b). During the mask layout, MLLs with different grating strengths and chirp directions are implemented to quantify the impact of the grating designs.

4. CHARACTERIZATION AND DISCUSSION

The fabricated 20 GHz QD MLL chips are mounted on a copper heat sink and tested at a fixed stage temperature of 18°C. CW light-current-voltage (LIV) characteristics are first measured as shown in Fig. 4(a). The light is collected from the cleaved facets using an integration sphere. Attention is focused on three laser designs: laser A with uniform grating, κ = 260 cm⁻¹; laser B with apodized and positively chirped grating, CR = 0.032 nm/μm; laser C with apodized and negatively chirped grating, CR = -0.032 nm/μm. Lasers B and C both have κ = 260 cm⁻¹ at half-length of the grating. The steps at the laser thresholds are from the loss-bleaching effect in the SAs as intracavity power increases. All lasers have similar series resistance around 3.3 Ω. Compared to previous QD MLL lasers of similar length [12], these lasers show 10–20 mA higher threshold current. This is due to the additional optical loss and carrier surface recombination introduced by the taper transition between the active and passive WGs, warranting further optimization of the taper design. Among the three lasers, laser B and laser C have the lowest and highest threshold current, respectively. The positively chirped grating exhibits reflection peak at 1292 nm as opposed to 1293 nm for uniform grating and 1297 nm for negatively chirped grating. The differences in misalignment between the laser gain peaks and the grating reflection peaks explain the observed threshold differences.

To study the passive mode-locking (PML) behavior of the laser, the output spectrum was measured using an optical spectrum analyzer (Yokogawa, AQ6370C), RF performance was measured with an electrical spectrum analyzer (Rohde & Schwarz, FSU), and the autocorrelation pulse width was...
The optical spectra under the conditions for the narrowest pulses are shown in Fig. 4(b) for lasers A–C. The dispersion compensation effect of the negatively chirped grating can be immediately seen from the broadened PML bandwidth in laser C. The 3 dB (10 dB) bandwidths for lasers A and C are 0.15 nm (0.28 nm) and 0.4 nm (0.9 nm), accommodating 2 (2) and 3 (8) lines. Lasers A and B have similar locking bandwidths for lasers A and C are 0.15 nm (0.28 nm) and 0.4 nm (0.9 nm), accommodating 2 (2) and 3 (8) lines. Lasers A and B have similar locking bandwidths for lasers A and C.

Table 1. MLL Design and Performance Parameters

<table>
<thead>
<tr>
<th>Apodization</th>
<th>MLL A</th>
<th>MLL B</th>
<th>MLL C</th>
<th>MLL D</th>
<th>MLL E</th>
</tr>
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<tbody>
<tr>
<td>CR (nm/μm)</td>
<td>None</td>
<td>Gaussian</td>
<td>Gaussian</td>
<td>Gaussian</td>
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<tr>
<td>k at grating center (cm⁻¹)</td>
<td>0</td>
<td>0.032</td>
<td>-0.032</td>
<td>-0.016</td>
<td>-0.032</td>
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<tr>
<td>Threshold current (mA)</td>
<td>61</td>
<td>260</td>
<td>260</td>
<td>260</td>
<td>350</td>
</tr>
<tr>
<td>Wavelength at grating reflection peak (nm)</td>
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<td>1292.1</td>
<td>1297.2</td>
<td>1295.2</td>
<td>1288.4</td>
</tr>
<tr>
<td>Minimum pulse width (ps)</td>
<td>12.8</td>
<td>14.4</td>
<td>5.3</td>
<td>7.6</td>
<td>4.5</td>
</tr>
</tbody>
</table>
during apodization. This can be readily verified by comparing lasers C and E. Indeed, the strong grating in laser E helps generate the narrowest pulse in all tested QD MLLs, resulting in a factor of 2.8 improvement compared to laser A. To achieve strong \( \kappa \) in laser E, the grating recess is increased. Consequently, the average effective index of the grating decreases, causing significant blueshift of the grating reflection peak as shown in Table 1. Decreased effective index also means less WG confinement in GaAs and more mode overlap with the doped n-type cladding. The large grating recess also leads to more interaction between the optical mode and WG surfaces. These observations explain the increased threshold current in laser E. A deeply etched grating provides a good alternative to increase \( \kappa \) without these drawbacks.

5. CONCLUSION
We have designed and demonstrated a novel passive-active integration scheme for QD lasers on Si based on an offset quantum dot device platform. This platform is enabled by the development of a GaAs/AlGaAs selective etch, which ensures uniform WG geometries and eliminates the need for regrowth. The transition between the passive and active WGs is accomplished by a linear taper, whose design requires further investigation to reduce taper losses. The potential of the QD platform integration of the MLL active sections with dispersion-engineered gratings etched on the passive WGs. The experiment has shown that negatively chirped and apodized grating mirrors can compensate for the group delay dispersion generated by the active sections of the MLLs. By carefully designing the chirp rate and grating strength, a factor of 2.8 reduction in pulse width has been achieved. More investigations will be carried out using the frequency-resolved optical gating (FROG) technique to further assist the design and characterization of the chirped gratings. The next important step for the QD platform development will be the demonstration of integrated QD PICs on Si substrate. As the QD platform matures, we expect more QD laser devices such as distributed Bragg reflector (DBR) lasers, DBR lasers, and sampled grating distributed Bragg reflector (SGDBR) lasers, to be implemented in the future.


Disclosures. The authors declare no conflicts of interest.

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